

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

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SCANNED

Q.A

Q.A. CH

APPLICATION NO. 09/712246	CONT/PRIOR F	CLASS 883 714	SUBCLASS 744	ART UNIT 883 2133	EXAMINER Chaudry
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APPLICANTS

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TITLE

Semiconductor memory device with built-in self test circuit
operating at high rate

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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